



DC to 100 MHz Transimpedance Amplifier

The TDC-100 is FET based. A low noise transimpedance amplifier featuring only 2 μ Ap-p full bandwidth input equivalent noise and 80dB gain. [1V/100 μ A]

The output amplitude is ±4V maximum when un-terminated and ±2V maximum when terminated with 50 Ω impedance (recommended).

All ARI Amplifiers typically require a power supply and cabling. Recommended power supply is the ARI Model F-100PS Low Noise (±15V) Power Supply.

[The above specified gain is valid for a 50 Ω load.]

11950 Spruce Canyon Circle, PO Box 7427, Golden, CO 80403 USA (303) 463-5500 Facsimile (303) 463-5505 http://www.aricorp.com E-Mail: aricorp@aricorp.com